

at least one reset transistor having a first terminal coupled to said second terminal of said photosensor, a second terminal coupled to a reset potential, and a control terminal coupled to a reset line;

a plurality of transfer lines;

a plurality of transfer transistors each having a first terminal, a second terminal and a control terminal, said first terminal of each said transfer transistor coupled to said second terminal of said photosensor, said control terminal coupled to a separate one of said transfer lines, and

a plurality of storage nodes, each said storage node coupled to said second terminal of a separate one of said transfer transistors.

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59. (New) In a pixel sensor array disposed on a semiconductor substrate, an active pixel sensor comprising:

a photosensor having a first terminal and a plurality of second terminals, said first terminal coupled to a first reference potential;

at least one reset transistor having a first terminal coupled to said at least one of said plurality of second terminals of said photosensor, a second terminal coupled to a reset potential, and a control terminal coupled to a reset line;

a transfer line;

a plurality of transfer transistors each having a first terminal, a second terminal and a control terminal, said first terminal of each said transfer transistor